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of 12

Complete if Known

Application Number	
Filing Date	
First Named Inventor	Lefurgy
Art Unit	
Examiner Name	

Attorney Docket Number AUS920030894US1

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature Xtent **Date Considered** 4/12/2006

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet

2

of

2

<i>Complaint if Known</i>	
Application Number	
Filing Date	
First Named Inventor	Lefurgy
Art Unit	
Examiner Name	
Attorney Docket Number AUS920030894US1	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>JPS</i>		Mizumo, et al., "Chip OS: Open Power-management Platform to Overcome the Power Crisis in Future LSIs" IEEE ISSC Conference, 2001, USA	
<i>JPS</i>		Gunther, et al., "Managing the Impact of Increasing Microprocessor Power Consumption", Intel, 2001, USA, web publication at http://developer.intel.com	

Examiner Signature	<i>JPS</i>	Date Considered	4/12/2006
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